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**PCT LEGAL
ADMINISTRATION
In the United States Patent and Trademark Office**

National Phase Patent

Date: June 30, 2008

In re Application of: Sunaga et al.

Filed: 5/8/2006

For: Semiconductor Storage Device
and Burst Operation Method

Confirmation 5642
No.

Serial Number: 10/595,742

PCT No.: PCT/JP2004/016296

Art Unit: Not Assigned

Examiner: Not Assigned

PETITION UNDER 37 C.F.R. 1.182

Mail Stop PCT
Commissioner for Patents
ATTN: Office of PCT Legal Administration
P. O. Box 1450
Alexandria, VA 22313-1450

This petition is in regards to correspondence from the Office of PCT Legal Administration mailed June 6, 2008. Any fees required in entering this petition may be charged to Applicant's deposit account, 09-0456.

Background: On 08 May 2006, applicants filed electronically a U.S. national stage application PCT/JP2004/016296, which was assigned U.S. application number 10/595,742. The bibliographic data accompanying the electronic submission incorrectly identified the application as having international application number PCT/JP2004/006296. This was the result of an unintentional typographical error made during the electronic filing process.

On May 9, 2006, applicants filed a supplemental transmittal letter stating that the National Phase U.S. application number 10/595,742 was intended to correspond with PCT application number PCT/JP2004/016296. On that date applicants also filed a preliminary amendment adding claims 7 and 8.

Discussion:

- 1) The international application for which national stage entry was intended is: PCT/JP2004/016296.
- 2) The presence of the conflicting instructions was due to an unintentional typographical error during the electronic filing process.

- 3) Applicants are concurrently submitting a Transmittal Letter to correct the file record to correspond with international application number PCT/JP2004/016296. See appendix A.
- 4) Applicants are further submitting a declaration to correct deficiencies, namely that pages 1 and 2 are illegible. The corrected declaration pages are attached hereto in Appendix B.
- 5) Applicants are including the required \$400 petition fee concurrently herewith.

Applicants respectfully request this petition be granted in light of the above explanation. Please do not hesitate to call the undersigned at the number listed below if there are further questions.

Respectfully submitted,

For: Sunaga et al.

By: W. Ryon Harding

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APPENDIX A

National Phase Patent

In the United States Patent and Trademark Office

Date: June 30, 2008

In re Application of: Sunaga et. al

Filed: 05/08/2006

For: Semiconductor Storage Device and Burst Operation Method

Confirmation No. 5642

Serial Number: 10/595,742

PCT Application No.: PCT/JP2004/016296

Art Unit: Not Assigned

Examiner: Not Assigned

**CORRECTION OF PRIORITY PCT APPLICATION NUMBER FOR NATIONAL
STAGE APPLICATION**

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313

Sir:

National Stage Application No. 10/595,742 claims priority to PCT Application No. PCT/JP2004/016296 filed 04 November 2004, which claims a priority date of 06 November 2003. Due to an unintentional typographical error during electronic filing, the PCT Application No. from which the National Stage Application claimed priority was incorrect. The incorrect number entered in the bibliographic data at the time of electronic filing was PCT/JP2004/006296. However, the correct PCT Application No. is PCT/JP2004/016296. Any fees required in entering this response may be charged to Applicant's deposit account, 09-0456.

Please do not hesitate to call the undersigned at the number listed below if there are further questions.

Respectfully submitted,

For: Sunaga et al.

By: / W. R. Harding /

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APPENDIX B

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

Semiconductor Storage Device and Burst Operation Method

the specification of which: (check one)

☒ is attached hereto.

was filed on _____, as Application Serial No. _____ and was amended on _____.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, ' 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, ' 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s):

Number	Country	Day/Month/Year	Priority Claimed
2003-377485	Japan	06/11/03	Yes

I hereby claim the benefit under Title 35, United States Code, ' 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, ' 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, ' 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Prior U.S. Applications:

Serial No.	Filing Date	Status
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the

knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may

jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorneys and/or agents listed under Customer NO. 24241 to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

All attorneys and/or agents at Customer No. 24241

Send all correspondence to: **24241**
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